

XC9572XL-10VQG64C Xilinx

XC9572XL-10VQG64C Xilinx is a high-end CPLD chip developed and produced by Xilinx. Ideal for use in leading-edge communications and computing systems, where the high reliability and low power consumption of the devices are important. Therefore, the chip's high-performance, low-voltage application capabilities make it often in short supply in the market. For various shortages of electronic components, please contact XT-ShenZhen@!!!

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Product description

The XC9572XL-10VQG64C Xilinx is ideal for high-performance, low voltage applications in leading-edge communications and computing systems. Supports in-System Programming (ISP) and full IEEE Std 1149.1 (JTAG) boundary scanning, providing excellent debugging and design iteration capabilities for small packages. The device has a logic density range from 800 to 6400 available gates with 36 to 288 registers, each. Enhanced pin locking capability avoids costly board rework. In-system programming and high programmability levels across the commercial operation range provide hassle-free reconfiguration of system field upgrades. Extended data retention times enable longer, more reliable system operational lifespans. Advanced system features include output control and user-programmable ground pins to help reduce system noise.

Product features

- * Optimized for high-performance 3.3V systems
- * 5 ns pin-to-pin logic delay with internal system frequencies up to 208 MHz
- * Small form factor packages including VQFP, TQFP, and CSP (Chip Scale Package)
- * All packages are lead (Pb) free
- * Low power consumption operation
- * The 5V fault-tolerant I/O pin accepts 5V, 3.3V, and 2.5V signals * 3.3V or 2.5V output capability
- * Advanced 0.35-micron feature size CMOSFastFLASH technology
- * Advanced system features
- * Excellent pin locking and deployable linearity of fast connect II switch matrix

Shenzhen Zhen Rong Era Supply Chain Management Co., Ltd.

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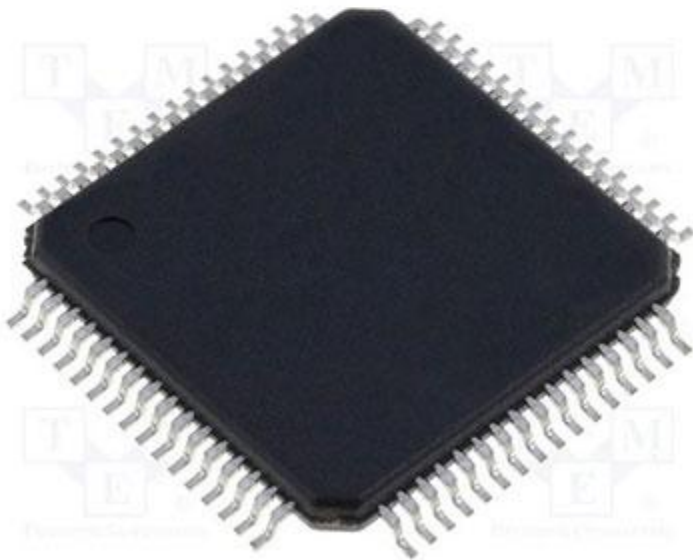
Phone: +86-13927462033

- * Ultra-wide 54 input function blocks
- * Up to 90 product terms per macro unit in a separate product term assignment
- * Local clock inversion with three global clocks and one product clock
- * Separate output enable for each output pin with local inversion
- * Enter hysteresis on all user and boundary-scan pin inputs
- * Bus hold circuit on all user pin inputs

Product parameters

Technical parameters	frequency	100MHz
	Supply voltage (DC).	3.30V
	The number of pins	64
	Number of I/O pins	52
	Number of inputs/outputs	52Input
	Operating Temperature (Max)	70°C
	Operating Temperature(Min)	0°C
	Supply voltage (Max)	3.6V
	Supply voltage (Min)	3V
	encapsulation	TQFP-64
	Packaging	Tray
	Leaded standard	Lead-Free

Product images



FAQ

Q: Are you a trader or a manufacturer?

A: Yes, we are traders. XC9572XL-10VQG64C Xilinx we sell are purchased directly from the factory.

Q: What is your MOQ?

A: We will decide according to the product packaging method, and will communicate with you in time before placing an order.

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Q: Are your shipments fast?

A: Yes, we have our own warehouse, and most of the goods are in stock. Same-day delivery can be achieved as soon as possible.